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ABSTRACT OF THE DISCLOSURE

A first processing circuit subjects image data output from an A/D converter circuit to a first signal process to produce first 5 image data. A second processing circuit subjects image data to a second signal process, which is independent of the first signal process, to produce second image data. The first and second processing circuits execute the first and second signal processes in a parallel manner to output the first and second image data 10 to a display device and a system microcomputer.